

A Mathematical Model of CPU

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Summary. This paper is based on a previous work of the first author [12] in which a mathematical model of the computer has been presented. The model deals with random access memory, such as RASP of C. C. Elgot and A. Robinson [11], however, it allows for a more realistic modeling of real computers. This new model of computers has been named by the author (Y. Nakamura, [12]) Architecture Model for Instructions (AMI). It is more developed than previous models, both in the description of hardware (e.g., the concept of the program counter, the structure of memory) as well as in the description of instructions (instruction codes, addresses). The structure of AMI over an arbitrary collection of mathematical domains N consists of:

- a non-empty set of objects,
- the instruction counter,
- a non-empty set of objects called instruction locations,
- a non-empty set of instruction codes,
- an instruction code for halting,
- a set of instructions that are ordered pairs with the first element being an instruction code and the second a finite sequence in which members are either objects of the AMI or elements of one of the domains included in N ,
- a function that assigns to every object of AMI its kind that is either *an instruction* or *an instruction location* or an element of N ,
- a function that assigns to every instruction its execution that is again a function mapping states of AMI into the set of states.

By a state of AMI we mean a function that assigns to every object of AMI an element of the same kind. In this paper we develop the theory of AMI. Some properties of AMI are introduced ensuring it to have some properties of real computers:

- a von Neumann AMI, in which only addresses to instruction locations are stored in the program counter,
- data oriented, those in which instructions cannot be stored in data locations,
- halting, in which the execution of the halt instruction is the identity mapping of the states of an AMI,
- steady programmed, the condition in which the contents of the instruction locations do not change during execution,

¹The work has been done while the second author was visiting Nagano in autumn 1992.

- definite, a property in which only instructions may be stored in instruction locations.

We present an example of AMI called a Small Concrete Model which has been constructed in [12]. The Small Concrete Model has only one kind of data: integers and a set of instructions, small but sufficient to cope with integers.

MML Identifier: **AMI_1**.

The terminology and notation used here have been introduced in the following articles: [19], [5], [6], [15], [2], [20], [14], [3], [17], [16], [10], [1], [4], [18], [13], [7], [9], [21], and [8].

1. PRELIMINARIES

In the sequel x is arbitrary. Next we state several propositions:

- (1) $\mathbb{N} \neq \mathbb{Z}$.
- (2) For arbitrary a, b holds $1 \neq \langle a, b \rangle$.
- (3) For arbitrary a, b holds $2 \neq \langle a, b \rangle$.
- (4) For arbitrary a, b, c, d and for every function g such that $\text{dom } g = \{a, b\}$ and $g(a) = c$ and $g(b) = d$ holds $g = [a \mapsto c, b \mapsto d]$.
- (5) For arbitrary a, b, c, d such that $a \neq b$ holds $\prod[a \mapsto \{c\}, b \mapsto \{d\}] = \{[a \mapsto c, b \mapsto d]\}$.

Let A be a set, and let B be a non-empty set. Then $A \cup B$ is a non-empty set. Let A be a non-empty set, and let B be a set. Then $A \cup B$ is a non-empty set. A set has non-empty elements if:

(Def.1) $\emptyset \notin \text{it}$.

One can verify that there exists a set which is non-empty with and non-empty elements.

Let A be a non-empty set. Then $\{A\}$ is a non-empty set with non-empty elements. Let B be a non-empty set. Then $\{A, B\}$ is a non-empty set with non-empty elements. Let A, B be non-empty sets with non-empty elements. Then $A \cup B$ is a non-empty set with non-empty elements.

2. GENERAL CONCEPTS

In the sequel N will be a non-empty set with non-empty elements.

We now define several new constructions. Let us consider N . We consider AMI's over N which are systems

\langle objects, a instruction counter, instruction locations, instruction codes, a halt instruction, instructions, a object kind, a execution \rangle , where the objects constitute a non-empty set, the instruction counter is an element of the objects, the instruction locations constitute a non-empty subset of the objects, the instruction codes constitute a non-empty set, the halt instruction is an element of the instruction codes, the instructions constitute a non-empty subset of $\{$ the instruction codes, $(\bigcup N \cup \text{the objects})^*$ $\}$, the object kind is a function from the objects into $N \cup \{$ the instructions, the instruction locations $\}$, and the execution is a function from the instructions into $(\prod(\text{the object kind}))^{\prod(\text{the object kind})}$. Let us consider N , and let S be an AMI over N . An object of S is an element of the objects of S .

An instruction of S is an element of the instructions of S .

An instruction-location of S is an element of the instruction locations of S .

Let us consider N , and let S be an AMI over N . The functor \mathbf{IC}_S yields an object of S and is defined by:

(Def.2) $\mathbf{IC}_S =$ the instruction counter of S .

Let us consider N , and let S be an AMI over N , and let o be an object of S . The functor $\text{ObjectKind}(o)$ yielding an element of $N \cup \{$ the instructions of S , the instruction locations of S $\}$ is defined by:

(Def.3) $\text{ObjectKind}(o) = (\text{the object kind of } S)(o)$.

Let A be a set, and let B be a non-empty set with non-empty elements, and let f be a function from A into B . Then $\prod f$ is a non-empty set of functions. Let P be a non-empty set of functions. We see that the element of P is a function. Let us consider N , and let S be an AMI over N . A state of S is an element of $\prod(\text{the object kind of } S)$.

Let us consider N , and let S be an AMI over N , and let I be an instruction of S , and let s be a state of S . The functor $\text{Exec}(I, s)$ yielding a state of S is defined by:

(Def.4) $\text{Exec}(I, s) = (\text{the execution of } S \text{ qua a function from the instructions of } S \text{ into } (\prod(\text{the object kind of } S))^{\prod(\text{the object kind of } S)})(I)(s)$.

Let us consider N , and let S be an AMI over N satisfying the condition: \langle the halt instruction of S , ε $\rangle \in$ the instructions of S . The functor \mathbf{halt}_S yields an instruction of S and is defined as follows:

(Def.5) $\mathbf{halt}_S = \langle$ the halt instruction of S , ε \rangle .

Let us consider N . An AMI over N is von Neumann if:

(Def.6) $\text{ObjectKind}(\mathbf{IC}_{\text{it}}) =$ the instruction locations of it.

An AMI over N is data-oriented if:

(Def.7) $(\text{the object kind of it})^{-1} \{$ the instructions of it $\} \subseteq$ the instruction locations of it.

An AMI over N is halting if:

(Def.8) for every state s of it holds $\text{Exec}(\mathbf{halt}_{\text{it}}, s) = s$.

An AMI over N is steady-programmed if:

(Def.9) for every state s of it and for every instruction i of it and for every instruction-location l of it holds $(\text{Exec}(i, s))(l) = s(l)$.

An AMI over N is definite if:

(Def.10) for every instruction-location l of it holds $\text{ObjectKind}(l) =$ the instructions of it.

Let us consider N . Note that there exists a von Neumann data-oriented halting steady-programmed definite strict AMI over N .

Let us consider N , and let S be a von Neumann AMI over N , and let s be a state of S . The functor \mathbf{IC}_s yields an instruction-location of S and is defined as follows:

(Def.11) $\mathbf{IC}_s = s(\mathbf{IC}_S)$.

3. A SMALL CONCRETE MODEL

In the sequel i, k will be natural numbers. We now define four new functors. The non-empty subset Loc_{SCM} of \mathbb{N} is defined by:

(Def.12) $\text{Loc}_{\text{SCM}} = \mathbb{N} \setminus \{0\}$.

The element Halt_{SCM} of \mathbb{Z}_9 is defined as follows:

(Def.13) $\text{Halt}_{\text{SCM}} = 0$.

The non-empty subset $\text{Data-Loc}_{\text{SCM}}$ of Loc_{SCM} is defined as follows:

(Def.14) $\text{Data-Loc}_{\text{SCM}} = \{2 \cdot k + 1\}$.

The non-empty subset $\text{Instr-Loc}_{\text{SCM}}$ of \mathbb{N} is defined by:

(Def.15) $\text{Instr-Loc}_{\text{SCM}} = \{2 \cdot k : k > 0\}$.

We adopt the following convention: I, J, K are elements of \mathbb{Z}_9 , a, a_1, a_2 are elements of $\text{Instr-Loc}_{\text{SCM}}$, and b, b_1, b_2, c, c_1 are elements of $\text{Data-Loc}_{\text{SCM}}$. The non-empty subset $\text{Instr}_{\text{SCM}}$ of $[\mathbb{Z}_9, \cup\{\mathbb{Z} \cup \mathbb{N}^*\}]$ is defined as follows:

(Def.16) $\text{Instr}_{\text{SCM}} = \{\langle \text{Halt}_{\text{SCM}}, \varepsilon \rangle\} \cup \{\langle J, \langle a \rangle \rangle : J = 6\} \cup \{\langle K, \langle a_1, b_1 \rangle \rangle : K \in \{7, 8\}\} \cup \{\langle I, \langle b, c \rangle \rangle : I \in \{1, 2, 3, 4, 5\}\}$.

The following propositions are true:

(6) $\text{Instr}_{\text{SCM}} = \{\langle \text{Halt}_{\text{SCM}}, \varepsilon \rangle\} \cup \{\langle J, \langle a \rangle \rangle : J = 6\} \cup \{\langle K, \langle a_1, b_1 \rangle \rangle : K \in \{7, 8\}\} \cup \{\langle I, \langle b, c \rangle \rangle : I \in \{1, 2, 3, 4, 5\}\}$.

(7) $\langle 0, \varepsilon \rangle \in \text{Instr}_{\text{SCM}}$.

(8) $\langle 6, \langle a_2 \rangle \rangle \in \text{Instr}_{\text{SCM}}$.

(9) If $x \in \{7, 8\}$, then $\langle x, \langle a_2, b_2 \rangle \rangle \in \text{Instr}_{\text{SCM}}$.

(10) If $x \in \{1, 2, 3, 4, 5\}$, then $\langle x, \langle b_1, c_1 \rangle \rangle \in \text{Instr}_{\text{SCM}}$.

The function OK_{SCM} from \mathbb{N} into $\{\mathbb{Z}\} \cup \{\text{Instr}_{\text{SCM}}, \text{Instr-Loc}_{\text{SCM}}\}$ is defined by:

(Def.17) $\text{OK}_{\text{SCM}}(0) = \text{Instr-Loc}_{\text{SCM}}$ and for every natural number k holds $\text{OK}_{\text{SCM}}(2 \cdot k + 1) = \mathbb{Z}$ and $\text{OK}_{\text{SCM}}(2 \cdot k + 2) = \text{Instr}_{\text{SCM}}$.

The following four propositions are true:

- (11) $\text{Instr-Loc}_{\text{SCM}} \neq \mathbb{Z}$ and $\text{Instr}_{\text{SCM}} \neq \mathbb{Z}$ and $\text{Instr-Loc}_{\text{SCM}} \neq \text{Instr}_{\text{SCM}}$.
- (12) For every i holds $\text{OK}_{\text{SCM}}(i) = \text{Instr-Loc}_{\text{SCM}}$ if and only if $i = 0$.
- (13) For every i holds $\text{OK}_{\text{SCM}}(i) = \mathbb{Z}$ if and only if there exists k such that $i = 2 \cdot k + 1$.
- (14) For every i holds $\text{OK}_{\text{SCM}}(i) = \text{Instr}_{\text{SCM}}$ if and only if there exists k such that $i = 2 \cdot k + 2$.

A $\text{states}_{\text{SCM}}$ is an element of $\prod(\text{OK}_{\text{SCM}})$.

In the sequel s is a $\text{states}_{\text{SCM}}$. We now state several propositions:

- (15) For every element a of $\text{Data-Loc}_{\text{SCM}}$ holds $\text{OK}_{\text{SCM}}(a) = \mathbb{Z}$.
- (16) For every element a of $\text{Instr-Loc}_{\text{SCM}}$ holds $\text{OK}_{\text{SCM}}(a) = \text{Instr}_{\text{SCM}}$.
- (17) For every element a of $\text{Instr-Loc}_{\text{SCM}}$ and for every element t of $\text{Data-Loc}_{\text{SCM}}$ holds $a \neq t$.
- (18) $\pi_0 \prod(\text{OK}_{\text{SCM}}) = \text{Instr-Loc}_{\text{SCM}}$.
- (19) For every element a of $\text{Data-Loc}_{\text{SCM}}$ holds $\pi_a \prod(\text{OK}_{\text{SCM}}) = \mathbb{Z}$.
- (20) For every element a of $\text{Instr-Loc}_{\text{SCM}}$ holds $\pi_a \prod(\text{OK}_{\text{SCM}}) = \text{Instr}_{\text{SCM}}$.

We now define two new functors. Let s be a $\text{states}_{\text{SCM}}$. The functor \mathbf{IC}_s yielding an element of $\text{Instr-Loc}_{\text{SCM}}$ is defined by:

(Def.18) $\mathbf{IC}_s = s(0)$.

Let s be a $\text{states}_{\text{SCM}}$, and let u be an element of $\text{Instr-Loc}_{\text{SCM}}$. The functor $\text{Chg}_{\text{SCM}}(s, u)$ yields a $\text{states}_{\text{SCM}}$ and is defined as follows:

(Def.19) $\text{Chg}_{\text{SCM}}(s, u) = s + \cdot (0 \dashrightarrow u)$.

The following three propositions are true:

- (21) For every $\text{states}_{\text{SCM}}$ s and for every element u of $\text{Instr-Loc}_{\text{SCM}}$ holds $(\text{Chg}_{\text{SCM}}(s, u))(0) = u$.
- (22) For every $\text{states}_{\text{SCM}}$ s and for every element u of $\text{Instr-Loc}_{\text{SCM}}$ and for every element m_1 of $\text{Data-Loc}_{\text{SCM}}$ holds $(\text{Chg}_{\text{SCM}}(s, u))(m_1) = s(m_1)$.
- (23) For every $\text{states}_{\text{SCM}}$ s and for all elements u, v of $\text{Instr-Loc}_{\text{SCM}}$ holds $(\text{Chg}_{\text{SCM}}(s, u))(v) = s(v)$.

Let s be a $\text{states}_{\text{SCM}}$, and let t be an element of $\text{Data-Loc}_{\text{SCM}}$, and let u be an integer. The functor $\text{Chg}_{\text{SCM}}(s, t, u)$ yielding a $\text{states}_{\text{SCM}}$ is defined by:

(Def.20) $\text{Chg}_{\text{SCM}}(s, t, u) = s + \cdot (t \dashrightarrow u)$.

The following four propositions are true:

- (24) For every $\text{states}_{\text{SCM}}$ s and for every element t of $\text{Data-Loc}_{\text{SCM}}$ and for every integer u holds $(\text{Chg}_{\text{SCM}}(s, t, u))(0) = s(0)$.
- (25) For every $\text{states}_{\text{SCM}}$ s and for every element t of $\text{Data-Loc}_{\text{SCM}}$ and for every integer u holds $(\text{Chg}_{\text{SCM}}(s, t, u))(t) = u$.
- (26) For every $\text{states}_{\text{SCM}}$ s and for every element t of $\text{Data-Loc}_{\text{SCM}}$ and for every integer u and for every element m_1 of $\text{Data-Loc}_{\text{SCM}}$ such that $m_1 \neq t$ holds $(\text{Chg}_{\text{SCM}}(s, t, u))(m_1) = s(m_1)$.

- (27) For every state_{SCM} s and for every element t of Data-Loc_{SCM} and for every integer u and for every element v of Instr-Loc_{SCM} holds
 $(\text{Chg}_{\text{SCM}}(s, t, u))(v) = s(v)$.

We now define two new functors. Let x be an element of Instr_{SCM}. Let us assume that there exist m_1, m_2 of the type elements of Data-Loc_{SCM}; I such that $x = \langle I, \langle m_1, m_2 \rangle \rangle$. The functor $x\text{address}_1$ yields an element of Data-Loc_{SCM} and is defined by:

- (Def.21) there exists a finite sequence f of elements of Data-Loc_{SCM} such that
 $f = x_2$ and $x\text{address}_1 = \pi_1 f$.

The functor $x\text{address}_2$ yields an element of Data-Loc_{SCM} and is defined by:

- (Def.22) there exists a finite sequence f of elements of Data-Loc_{SCM} such that
 $f = x_2$ and $x\text{address}_2 = \pi_2 f$.

One can prove the following proposition

- (28) For every element x of Instr_{SCM} and for all elements m_1, m_2 of Data-Loc_{SCM} and for every I such that $x = \langle I, \langle m_1, m_2 \rangle \rangle$
holds $x\text{address}_1 = m_1$ and $x\text{address}_2 = m_2$.

Let x be an element of Instr_{SCM}. Let us assume that there exist m_1 of the type an element of Instr-Loc_{SCM}; I such that $x = \langle I, \langle m_1 \rangle \rangle$. The functor $x\text{address}_j$ yielding an element of Instr-Loc_{SCM} is defined as follows:

- (Def.23) there exists a finite sequence f of elements of Instr-Loc_{SCM} such that
 $f = x_2$ and $x\text{address}_j = \pi_1 f$.

We now state the proposition

- (29) For every element x of Instr_{SCM} and for every element m_1 of Instr-Loc_{SCM} and for every I such that $x = \langle I, \langle m_1 \rangle \rangle$ holds $x\text{address}_j = m_1$.

We now define two new functors. Let x be an element of Instr_{SCM}. Let us assume that there exist m_1 of the type an element of Instr-Loc_{SCM}; m_2 of the type an element of Data-Loc_{SCM}; I such that $x = \langle I, \langle m_1, m_2 \rangle \rangle$. The functor $x\text{address}_j$ yields an element of Instr-Loc_{SCM} and is defined as follows:

- (Def.24) there exists an element m_1 of Instr-Loc_{SCM} and there exists an element m_2 of Data-Loc_{SCM} such that $\langle m_1, m_2 \rangle = x_2$ and $x\text{address}_j = \pi_1 \langle m_1, m_2 \rangle$.

The functor $x\text{address}_c$ yielding an element of Data-Loc_{SCM} is defined by:

- (Def.25) there exists an element m_1 of Instr-Loc_{SCM} and there exists an element m_2 of Data-Loc_{SCM} such that $\langle m_1, m_2 \rangle = x_2$ and $x\text{address}_c = \pi_2 \langle m_1, m_2 \rangle$.

The following proposition is true

- (30) For every element x of Instr_{SCM} and for every element m_1 of Instr-Loc_{SCM} and for every element m_2 of Data-Loc_{SCM} and for every I such that
 $x = \langle I, \langle m_1, m_2 \rangle \rangle$ holds $x\text{address}_j = m_1$ and $x\text{address}_c = m_2$.

We now define five new functors. Let s be a state_{SCM}, and let a be an element of Data-Loc_{SCM}. Then $s(a)$ is an integer. Let D be a non-empty set, and let x, y be arbitrary, and let a, b be elements of D . Then $(x = y \rightarrow a, b)$ is an element

of D . Let D be a non-empty set, and let x, y be real numbers, and let a, b be elements of D . The functor $(x > y \rightarrow a, b)$ yields an element of D and is defined as follows:

(Def.26)

$$(x > y \rightarrow a, b) = \begin{cases} a, & \text{if } x > y, \\ b, & \text{otherwise.} \end{cases}$$

Let d be an element of $\text{Instr-Loc}_{\text{SCM}}$. The functor $\text{Next}(d)$ yields an element of $\text{Instr-Loc}_{\text{SCM}}$ and is defined as follows:

(Def.27) $\text{Next}(d) = d + 2$.

Let x be an element of $\text{Instr}_{\text{SCM}}$, and let s be a $\text{states}_{\text{SCM}}$. The functor

$\text{Exec-Ress}_{\text{SCM}}(x, s)$ yielding a $\text{states}_{\text{SCM}}$ is defined as follows:

- (Def.28) (i) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(\text{Chg}_{\text{SCM}}(s, x\text{address}_1, s(x\text{address}_2)), \text{Next}(\mathbf{IC}_s))$ if there exist elements m_1, m_2 of $\text{Data-Loc}_{\text{SCM}}$ such that $x = \langle 1, \langle m_1, m_2 \rangle \rangle$,
- (ii) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(\text{Chg}_{\text{SCM}}(s, x\text{address}_1, s(x\text{address}_1) + s(x\text{address}_2)), \text{Next}(\mathbf{IC}_s))$ if there exist elements m_1, m_2 of $\text{Data-Loc}_{\text{SCM}}$ such that $x = \langle 2, \langle m_1, m_2 \rangle \rangle$,
- (iii) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(\text{Chg}_{\text{SCM}}(s, x\text{address}_1, s(x\text{address}_1) - s(x\text{address}_2)), \text{Next}(\mathbf{IC}_s))$ if there exist elements m_1, m_2 of $\text{Data-Loc}_{\text{SCM}}$ such that $x = \langle 3, \langle m_1, m_2 \rangle \rangle$,
- (iv) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(\text{Chg}_{\text{SCM}}(s, x\text{address}_1, s(x\text{address}_1) \cdot s(x\text{address}_2)), \text{Next}(\mathbf{IC}_s))$ if there exist elements m_1, m_2 of $\text{Data-Loc}_{\text{SCM}}$ such that $x = \langle 4, \langle m_1, m_2 \rangle \rangle$,
- (v) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(\text{Chg}_{\text{SCM}}(\text{Chg}_{\text{SCM}}(s, x\text{address}_1, s(x\text{address}_1) \div s(x\text{address}_2)), x\text{address}_2, s(x\text{address}_1) \bmod s(x\text{address}_2)), \text{Next}(\mathbf{IC}_s))$ if there exist elements m_1, m_2 of $\text{Data-Loc}_{\text{SCM}}$ such that $x = \langle 5, \langle m_1, m_2 \rangle \rangle$,
- (vi) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(s, x\text{address}_j)$ if there exists an element m_1 of $\text{Instr-Loc}_{\text{SCM}}$ such that $x = \langle 6, \langle m_1 \rangle \rangle$,
- (vii) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(s, (s(x\text{address}_c) = 0 \rightarrow x\text{address}_j, \text{Next}(\mathbf{IC}_s)))$ if there exists an element m_1 of $\text{Instr-Loc}_{\text{SCM}}$ and there exists an element m_2 of $\text{Data-Loc}_{\text{SCM}}$ such that $x = \langle 7, \langle m_1, m_2 \rangle \rangle$,
- (viii) $\text{Exec-Ress}_{\text{SCM}}(x, s) = \text{Chg}_{\text{SCM}}(s, (s(x\text{address}_c) > 0 \rightarrow x\text{address}_j, \text{Next}(\mathbf{IC}_s)))$ if there exists an element m_1 of $\text{Instr-Loc}_{\text{SCM}}$ and there exists an element m_2 of $\text{Data-Loc}_{\text{SCM}}$ such that $x = \langle 8, \langle m_1, m_2 \rangle \rangle$,
- (ix) $\text{Exec-Ress}_{\text{SCM}}(x, s) = s$, otherwise.

The function Exec_{SCM} from $\text{Instr}_{\text{SCM}}$ into $\prod \text{OK}_{\text{SCM}}^{\prod \text{OK}_{\text{SCM}}}$ is defined by:

(Def.29) for every element x of $\text{Instr}_{\text{SCM}}$ and for every $\text{states}_{\text{SCM}}$ y holds $(\text{Exec}_{\text{SCM}}(x) \mathbf{qua}$ an element of $(\prod(\text{OK}_{\text{SCM}}))^{\prod(\text{OK}_{\text{SCM}})}(y) = \text{Exec-Ress}_{\text{SCM}}(x, y)$.

The von Neumann strict AMI **SCM** is defined by:

(Def.30) $\mathbf{SCM} = \langle \mathbb{N}, 0, \text{Instr-Loc}_{\mathbf{SCM}}, \mathbb{Z}_9, \text{Halt}_{\mathbf{SCM}}, \text{Instr}_{\mathbf{SCM}}, \text{OK}_{\mathbf{SCM}}, \text{Exec}_{\mathbf{SCM}} \rangle$.

Next we state three propositions:

(31) \mathbf{SCM} is data-oriented.

(32) \mathbf{SCM} is definite.

(33) The objects of $\mathbf{SCM} = \mathbb{N}$ and the instruction counter of $\mathbf{SCM} = 0$ and the instruction locations of $\mathbf{SCM} = \text{Instr-Loc}_{\mathbf{SCM}}$ and the instruction codes of $\mathbf{SCM} = \mathbb{Z}_9$ and the halt instruction of $\mathbf{SCM} = \text{Halt}_{\mathbf{SCM}}$ and the instructions of $\mathbf{SCM} = \text{Instr}_{\mathbf{SCM}}$ and the object kind of $\mathbf{SCM} = \text{OK}_{\mathbf{SCM}}$ and the execution of $\mathbf{SCM} = \text{Exec}_{\mathbf{SCM}}$.

An object of \mathbf{SCM} is said to be a data-location if:

(Def.31) $it \in \text{Data-Loc}_{\mathbf{SCM}}$.

Let s be a state of \mathbf{SCM} , and let d be a data-location. Then $s(d)$ is an integer.

We adopt the following convention: a, b, c denote data-locations, l_1 denotes an instruction-location of \mathbf{SCM} , and I denotes an instruction of \mathbf{SCM} . We now define several new functors. Let us consider a, b . The functor $a:=b$ yielding an instruction of \mathbf{SCM} is defined by:

(Def.32) $a:=b = \langle 1, \langle a, b \rangle \rangle$.

The functor $\text{AddTo}(a, b)$ yielding an instruction of \mathbf{SCM} is defined by:

(Def.33) $\text{AddTo}(a, b) = \langle 2, \langle a, b \rangle \rangle$.

The functor $\text{SubFrom}(a, b)$ yielding an instruction of \mathbf{SCM} is defined by:

(Def.34) $\text{SubFrom}(a, b) = \langle 3, \langle a, b \rangle \rangle$.

The functor $\text{MultBy}(a, b)$ yields an instruction of \mathbf{SCM} and is defined by:

(Def.35) $\text{MultBy}(a, b) = \langle 4, \langle a, b \rangle \rangle$.

The functor $\text{Divide}(a, b)$ yields an instruction of \mathbf{SCM} and is defined as follows:

(Def.36) $\text{Divide}(a, b) = \langle 5, \langle a, b \rangle \rangle$.

Let us consider l_1 . The functor $\text{goto } l_1$ yields an instruction of \mathbf{SCM} and is defined by:

(Def.37) $\text{goto } l_1 = \langle 6, \langle l_1 \rangle \rangle$.

Let us consider a . The functor **if** $a = 0$ **goto** l_1 yielding an instruction of \mathbf{SCM} is defined as follows:

(Def.38) **if** $a = 0$ **goto** $l_1 = \langle 7, \langle l_1, a \rangle \rangle$.

The functor **if** $a > 0$ **goto** l_1 yields an instruction of \mathbf{SCM} and is defined as follows:

(Def.39) **if** $a > 0$ **goto** $l_1 = \langle 8, \langle l_1, a \rangle \rangle$.

In the sequel s will denote a state of \mathbf{SCM} . Next we state two propositions:

(34) $\mathbf{IC}_{\mathbf{SCM}} = 0$.

(35) For every $\text{states}_{\mathbf{SCM}}$ S such that $S = s$ holds $\mathbf{IC}_s = \mathbf{IC}_S$.

Let l_1 be an instruction-location of \mathbf{SCM} . The functor $\text{Next}(l_1)$ yielding an instruction-location of \mathbf{SCM} is defined by:

(Def.40) there exists an element m_3 of $\text{Instr-Loc}_{\text{SCM}}$ such that $m_3 = l_1$ and $\text{Next}(l_1) = \text{Next}(m_3)$.

Next we state two propositions:

- (36) For every instruction-location l_1 of **SCM** and for every element m_3 of $\text{Instr-Loc}_{\text{SCM}}$ such that $m_3 = l_1$ holds $\text{Next}(m_3) = \text{Next}(l_1)$.
- (37) For every element i of $\text{Instr}_{\text{SCM}}$ such that $i = I$ and for every state $_{\text{SCM}}$ S such that $S = s$ holds $\text{Exec}(I, s) = \text{Exec-Res}_{\text{SCM}}(i, S)$.

4. USERS GUIDE

One can prove the following propositions:

- (38) $(\text{Exec}(a:=b, s))(\mathbf{IC}_{\text{SCM}}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(a:=b, s))(a) = s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(a:=b, s))(c) = s(c)$.
- (39) $(\text{Exec}(\text{AddTo}(a, b), s))(\mathbf{IC}_{\text{SCM}}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{AddTo}(a, b), s))(a) = s(a) + s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(\text{AddTo}(a, b), s))(c) = s(c)$.
- (40) $(\text{Exec}(\text{SubFrom}(a, b), s))(\mathbf{IC}_{\text{SCM}}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{SubFrom}(a, b), s))(a) = s(a) - s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(\text{SubFrom}(a, b), s))(c) = s(c)$.
- (41) $(\text{Exec}(\text{MultBy}(a, b), s))(\mathbf{IC}_{\text{SCM}}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{MultBy}(a, b), s))(a) = s(a) \cdot s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(\text{MultBy}(a, b), s))(c) = s(c)$.
- (42) Suppose $a \neq b$. Then
- (i) $(\text{Exec}(\text{Divide}(a, b), s))(\mathbf{IC}_{\text{SCM}}) = \text{Next}(\mathbf{IC}_s)$,
 - (ii) $(\text{Exec}(\text{Divide}(a, b), s))(a) = s(a) \div s(b)$,
 - (iii) $(\text{Exec}(\text{Divide}(a, b), s))(b) = s(a) \bmod s(b)$,
 - (iv) for every c such that $c \neq a$ and $c \neq b$ holds $(\text{Exec}(\text{Divide}(a, b), s))(c) = s(c)$.
- (43) $(\text{Exec}(\text{goto } l_1, s))(\mathbf{IC}_{\text{SCM}}) = l_1$ and $(\text{Exec}(\text{goto } l_1, s))(c) = s(c)$.
- (44) If $s(a) = 0$, then $(\text{Exec}(\text{if } a = 0 \text{ goto } l_1, s))(\mathbf{IC}_{\text{SCM}}) = l_1$ and also if $s(a) \neq 0$, then $(\text{Exec}(\text{if } a = 0 \text{ goto } l_1, s))(\mathbf{IC}_{\text{SCM}}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{if } a = 0 \text{ goto } l_1, s))(c) = s(c)$.
- (45) If $s(a) > 0$, then $(\text{Exec}(\text{if } a > 0 \text{ goto } l_1, s))(\mathbf{IC}_{\text{SCM}}) = l_1$ and also if $s(a) \leq 0$, then $(\text{Exec}(\text{if } a > 0 \text{ goto } l_1, s))(\mathbf{IC}_{\text{SCM}}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{if } a > 0 \text{ goto } l_1, s))(c) = s(c)$.
- (46) $\text{Exec}(\text{halts}_{\text{SCM}}, s) = s$.
- (47) For every state s of **SCM** and for every instruction-location i of **SCM** holds $s(i)$ is an instruction of **SCM**.

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Received October 14, 1992
